



(43) International Publication Date
6 May 2005 (06.05.2005)

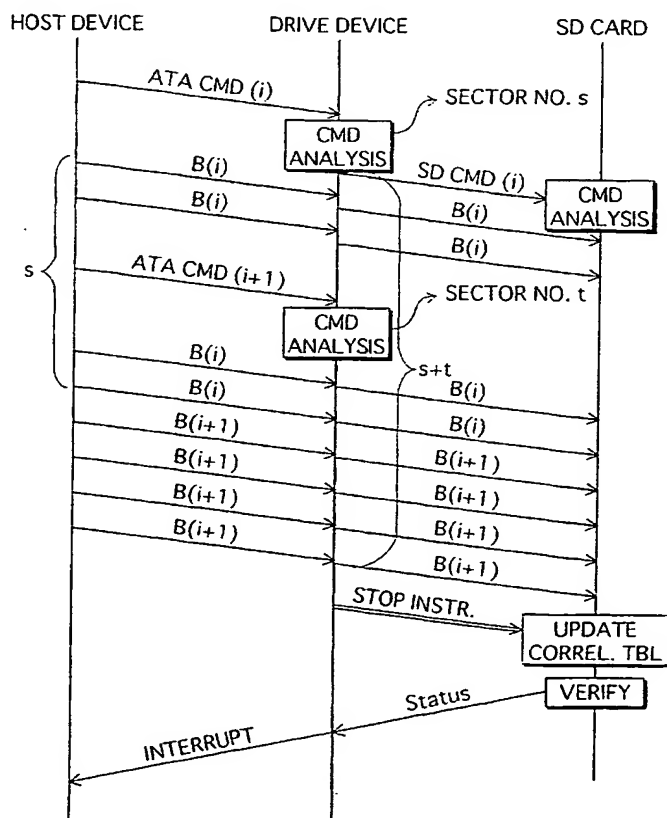
PCT

(10) International Publication Number
WO 2005/041207 A2

- (51) International Patent Classification⁷: G11C 16/10
- (21) International Application Number: PCT/JP2004/016365
- (22) International Filing Date: 28 October 2004 (28.10.2004)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:
2003-369554 29 October 2003 (29.10.2003) JP
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- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,

[Continued on next page]

(54) Title: DRIVE DEVICE AND RELATED COMPUTER PROGRAM



(57) Abstract: A drive device capable of preventing a drop in the data transfer rate, even when write instructions from a host device are given with commands having a short write data length. If the write end address of one of a plurality of ATA commands issued by a host device 2 is consecutive with the write start address of the next ATA command, a command analysis unit 11 has data writing to an SD card 1 by the consecutive commands performed in a single process. As a result, overheads when writing data to SD cards are incurred only once, allowing the transfer rate to be improved.



FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI,
SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ,
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